System on Module

The Spiderboard SoM is a programmable, non-volatile solution based on Intel® MAX®10 FPGA, which enables it to deliver full-featured FPGA capabilities: support for various soft-core CPUs, video-processing algorithms, etc. The Spiderboard SoM promotes the free and open design concept: KiCAD design files are available under CERN OHL v1.2.

This module can be considered as a cost-optimized alternative to the MX10 module.

Features:

- MAX 10 single voltage supply FPGA in U169 package
- module supports the following range of devices:
  from 10M02SC to 10M16SA
- optional 4 MiB SPI NOR
- optional 4 GiB e.MMC
- optional 8 MiB SDRAM
- FPGA GPIO pins
- optional RTC with battery backup
- optional Li-Ion/Li-Pol charger
- Size: 70mm x 35mm

OS Support:

- FreeRTOS™

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Baseboard

The Spiderboard Baseboard is a unique baseboard, designed to host the Spiderboard SoM / MX10 SoM based on Intel® (former Altera®) MAX®10 FPGA. Spiderboard Baseboard is available as a low cost and extremely flexible platform which enables user to setup a running system according to the required specification in a very short time. This baseboard has a large prototype area and is based on the free and open design concept: KiCAD design files are available under CERN OHL v1.2.
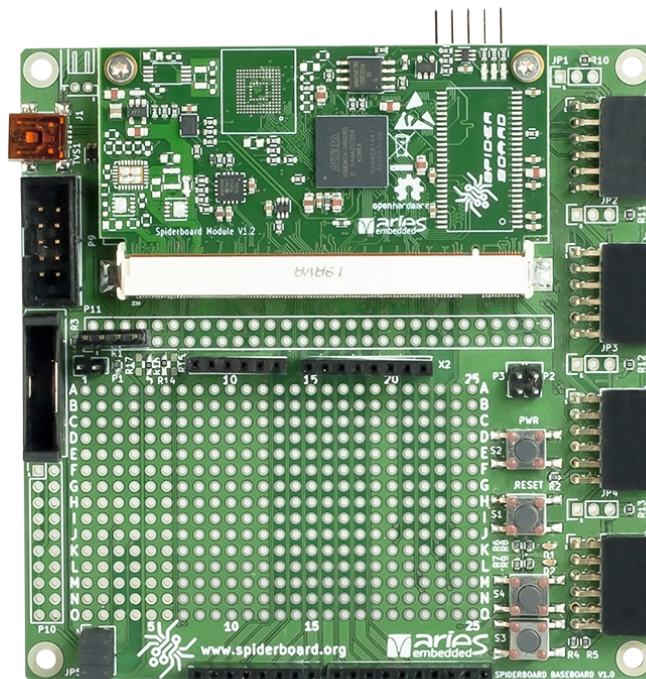
Features:

- simple baseboard in 2 layer design
- compatible with MX10 and Spiderboard SoM
- open hardware
- MxM2 pcb edge connector
- .1" grid 25x15 prototyping area
- 4 PMOD compatible **connectors**
- reset, power and 2 user push buttons
- 2 user LEDs
- Arduino shield compatible interface
- Intel USB Blaster programming interface
- USB mini B connector
- CR2032 cell holder
- JST-2.0 lithium battery connector
- configuration jumpers

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